

# Hybrid computing using a neural network with dynamic external memory

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Artificial neural networks are remarkably adept at sensory processing, sequence learning and reinforcement learning, but are limited in their ability to represent variables and data structures and to store data over long timescales, owing to the lack of an external memory. Here we introduce a machine learning model called a differentiable neural computer (DNC), which consists of a neural network that can read from and write to an external memory matrix, analogous to the random-access memory in a conventional computer. Like a conventional computer, it can use its memory to represent and manipulate complex data structures, but, like a neural network, it can learn to do so from data. When trained with supervised learning, we demonstrate that a DNC can successfully answer synthetic questions designed to emulate reasoning and inference problems in natural language. We show that it can learn tasks such as finding the shortest path between specified points and inferring the missing links in randomly generated graphs, and then generalize these tasks to specific graphs such as transport networks and family trees. When trained with reinforcement learning, a DNC can complete a moving blocks puzzle in which changing goals are specified by sequences of symbols. Taken together, our results demonstrate that DNCs have the capacity to solve complex, structured tasks that are inaccessible to neural networks without external read–write memory.

Modern computers separate computation and memory. Computation is performed by a processor, which can use an addressable memory to bring operands in and out of play. This confers two important benefits: the use of extensible storage to write new information and the ability to treat the contents of memory as variables. Variables are critical to algorithm generality: to perform the same procedure on one datum or another, an algorithm merely has to change the address it reads from. In contrast to computers, the computational and memory resources of artificial neural networks are mixed together in the network weights and neuron activity. This is a major liability: as the memory demands of a task increase, these networks cannot allocate new storage dynamically, nor easily learn algorithms that act independently of the values realized by the task variables.

Although recent breakthroughs demonstrate that neural networks are remarkably adept at sensory processing<sup>1</sup>, sequence learning<sup>2,3</sup> and reinforcement learning<sup>4</sup>, cognitive scientists and neuroscientists have argued that neural networks are limited in their ability to represent variables and data structures<sup>5–9</sup>, and to store data over long timescales without interference<sup>10,11</sup>. We aim to combine the advantages of neural and computational processing by providing a neural network with read–write access to external memory. The access is narrowly focused, minimizing interference among memoranda and enabling long-term storage<sup>12,13</sup>. The whole system is differentiable, and can therefore be trained end-to-end with gradient descent, allowing the network to learn how to operate and organize the memory in a goal-directed manner.

## System overview

A DNC is a neural network coupled to an external memory matrix. (The behaviour of the network is independent of the memory size as long as the memory is not filled to capacity, which is why we view the memory as ‘external’.) If the memory can be thought of as the DNC’s

RAM, then the network, referred to as the ‘controller’, is a differentiable CPU whose operations are learned with gradient descent. The DNC architecture differs from recent neural memory frameworks<sup>14,15</sup> in that the memory can be selectively written to as well as read, allowing iterative modification of memory content. An earlier form of DNC, the neural Turing machine<sup>16</sup>, had a similar structure, but more limited memory access methods (see Methods for further discussion).

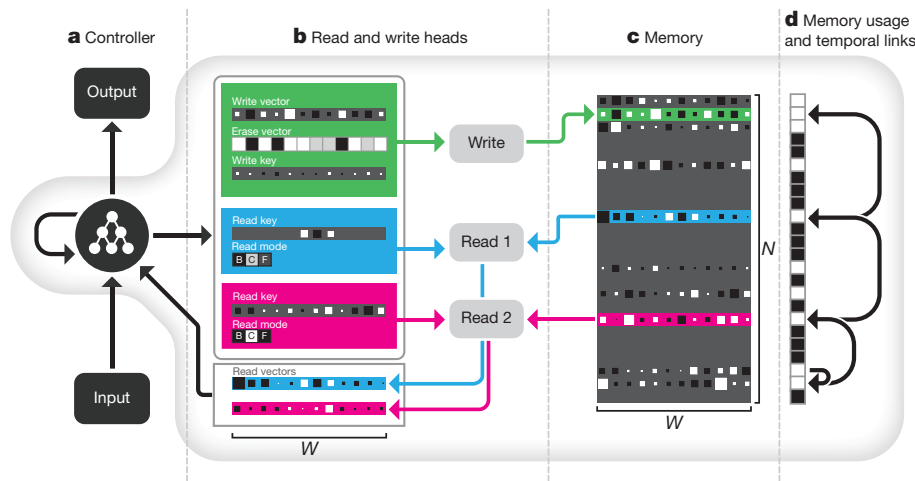
Whereas conventional computers use unique addresses to access memory contents, a DNC uses differentiable attention mechanisms<sup>2,16–18</sup> to define distributions over the  $N$  rows, or ‘locations’, in the  $N \times W$  memory matrix  $M$ . These distributions, which we call weightings, represent the degree to which each location is involved in a read or write operation. The read vector  $r$  returned by a read weighting  $w^r$  over memory  $M$  is a weighted sum over the memory locations:  $r = \sum_{i=1}^N M[i, :] w^r[i]$ , where the ‘ $\cdot$ ’ denotes all  $j = 1, \dots, W$ . Similarly, the write operation uses a write weighting  $w^w$  to first erase with an erase vector  $e$ , then add a write vector  $v$ :  $M[i, j] \leftarrow M[i, j] (1 - w^w[i] e[j]) + w^w[i] v[j]$ . The functional units that determine and apply the weightings are called read and write heads. The operation of the heads is illustrated in Fig. 1 and summarized below; see Methods for a formal description.

## Interaction between the heads and the memory

The heads use three distinct forms of differentiable attention. The first is content lookup<sup>16,17,19–21</sup>, in which a key vector emitted by the controller is compared to the content of each location in memory according to a similarity measure (here, cosine similarity). The similarity scores determine a weighting that can be used by the read heads for associative recall<sup>19</sup> or by the write head to modify an existing vector in memory. Importantly, a key that only partially matches the content of a memory location can still be used to attend strongly to that location.

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**Figure 1 | DNC architecture.** **a**, A recurrent controller network receives input from an external data source and produces output. **b**, **c**, The controller also outputs vectors that parameterize one write head (green) and multiple read heads (two in this case, blue and pink). (A reduced selection of parameters is shown.) The write head defines a write and an erase vector that are used to edit the  $N \times W$  memory matrix, whose elements' magnitudes and signs are indicated by box area and shading, respectively. Additionally, a write key is used for content lookup to find previously written locations to edit. The write key can contribute to

defining a weighting that selectively focuses the write operation over the rows, or locations, in the memory matrix. The read heads can use gates called read modes to switch between content lookup using a read key ('C') and reading out locations either forwards ('F') or backwards ('B') in the order they were written. **d**, The usage vector records which locations have been used so far, and a temporal link matrix records the order in which locations were written; here, we represent the order locations were written to using directed arrows.

This enables a form of pattern completion whereby the value recovered by reading the memory location includes additional information that is not present in the key. In general, key-value retrieval provides a rich mechanism for navigating associative data structures in the external memory, because the content of one address can effectively encode references to other addresses.

A second attention mechanism records transitions between consecutively written locations in an  $N \times N$  temporal link matrix  $L$ .  $L[i, j]$  is close to 1 if  $i$  was the next location written after  $j$ , and is close to 0 otherwise. For any weighting  $w$ , the operation  $Lw$  smoothly shifts the focus forwards to the locations written after those emphasized in  $w$ , whereas  $L^T w$  shifts the focus backwards. This gives a DNC the native ability to recover sequences in the order in which it wrote them, even when consecutive writes did not occur in adjacent time-steps.

The third form of attention allocates memory for writing. The 'usage' of each location is represented as a number between 0 and 1, and a weighting that picks out unused locations is delivered to the write head. As well as automatically increasing with each write to a location, usage can be decreased after each read. This allows the controller to reallocate memory that is no longer required (see Extended Data Fig. 1). The allocation mechanism is independent of the size and contents of the memory, meaning that DNCs can be trained to solve a task using one size of memory and later upgraded to a larger memory without retraining (Extended Data Fig. 2). In principle, this would make it possible to use an unbounded external memory by automatically increasing the number of locations every time the minimum usage of any location passes a certain threshold.

The design of the attention mechanisms was motivated largely by computational considerations. Content lookup enables the formation of associative data structures; temporal links enable sequential retrieval of input sequences; and allocation provides the write head with unused locations. However, there are interesting parallels between the memory mechanisms of a DNC and the functional capabilities of the mammalian hippocampus. DNC memory modification is fast and can be one-shot, resembling the associative long-term potentiation of hippocampal CA3 and CA1 synapses<sup>22</sup>. The hippocampal dentate gyrus, a region known to support neurogenesis<sup>23</sup>, has been proposed to increase representational sparsity, thereby enhancing memory capacity<sup>24</sup>: usage-based memory allocation and sparse weightings may provide similar

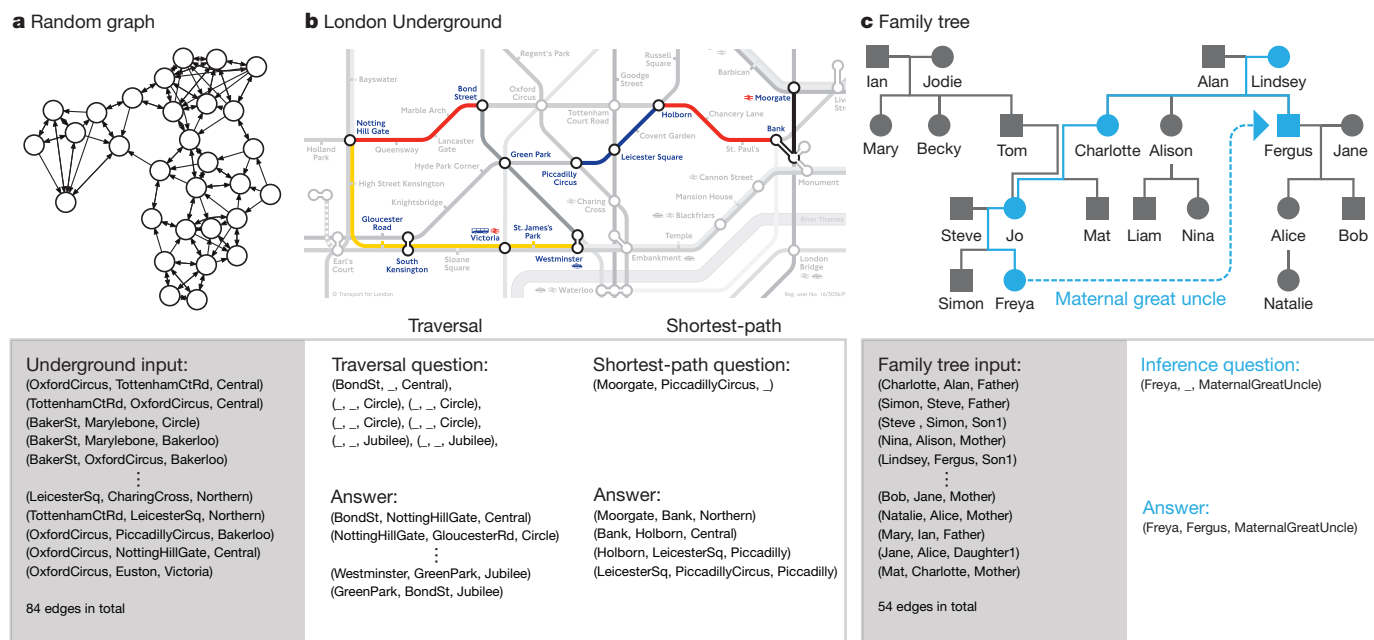
facilities in our model. Human 'free recall' experiments demonstrate the increased probability of item recall in the same order as first presented—a hippocampus-dependent phenomenon accounted for by the temporal context model<sup>25</sup>, bearing some similarity to the formation of temporal links (Methods).

### Synthetic question answering experiments

Our first experiments investigated the capacity of the DNC to perform question answering. To compare DNCs to other neural network architectures, we considered the bAbI dataset<sup>26</sup>, which includes 20 types of synthetically generated questions designed to mimic aspects of textual reasoning. The dataset consists of short 'story' snippets followed by questions with answers that can be inferred from the stories: for example, the story "John is in the playground. John picked up the football." followed by the question "Where is the football?" with answer "playground" requires a system to combine two supporting facts, whereas "Sheep are afraid of wolves. Gertrude is a sheep. Mice are afraid of cats. What is Gertrude afraid of?" (answer, "wolves") tests its facility at basic deduction (and resilience to distractors). We found that a single DNC, jointly trained on all 20 question types with 10,000 instances each, was able to achieve a mean test error rate of 3.8% with task failure (defined as  $>5\%$  error) on 2 types of questions, compared to 7.5% mean error and 6 failed tasks for the best previous jointly trained result<sup>21</sup>. We also found that DNCs performed much better than both long short-term memory<sup>27</sup> (LSTM; at present the benchmark neural network for most sequence processing tasks) and the neural Turing machine<sup>16</sup> (see Extended Data Table 1 for details). Unlike previous results on this dataset, the inputs to our model were single word tokens without any preprocessing or sentence-level features (see Methods for details).

### Graph experiments

Although bAbI is presented in natural language, each declarative sentence involves a limited vocabulary and is generated from a simple triple containing an actor, an action and a set of arguments. Such sentences could easily be rendered in graphical form: for example "John is in the playground" can be diagrammed as two named nodes, 'Playground' and 'John', connected by a named edge 'Contains'. In this sense, the propositional knowledge in many of the bAbI tasks is equivalent to a set of constraints on an underlying graph structure. Indeed, many important



**Figure 2 | Graph tasks.** **a**, An example of a randomly generated graph used for training. **b**, Zone 1 interchange stations of the London Underground map, used as a generalization test for the traversal and shortest-path tasks. Random seven-step traversals (an example of which is shown on the left) were tested, yielding an average accuracy of 98.8%. Testing on all possible four-step shortest paths (example shown on the right) gave an average accuracy of 55.3%. **c**, The family tree that was used as a generalization test for the inference task; four-step relations such as the one shown in

blue (from Freya to Fergus, her maternal great uncle) were tested, giving an average accuracy of 81.8%. The symbol sequences processed by the network during the test examples are shown beneath the graphs. The input is an unordered list of ('from node', 'to node', 'edge') triple vectors that describes the graph. For each task, the question is a sequence of triples with missing elements (denoted '\_') and the answer is a sequence of completed triples.

tasks faced by machine learning involve graph data, including parse trees, social networks, knowledge graphs and molecular structures. We therefore turn next to a set of synthetic reasoning experiments on randomly generated graphs.

Unlike bAbI, the edges in our graphs were presented explicitly, with each input vector specifying a triple consisting of two node labels and an edge label. We generated training graphs with random labelling and connectivity and defined three kinds of query: 'traversal', 'shortest path' and 'inference' (Fig. 2). After training with curriculum learning<sup>28,29</sup> using graphs and queries with gradually increasing complexity, the networks were tested (with no retraining) on two specific graphs as a test of generalization to realistic data: a symbolic map of the London Underground and an invented family tree.

For the traversal task (Fig. 2b), the network was instructed to report the node arrived at after leaving a start node and following a path of edges generated by a random walk. For the shortest-path task (Fig. 2b), a random start and end node were given as the query, and the network was asked to return a sequence of triples corresponding to a minimum-length path between them. Because we considered paths of up to length five, this is a harder version of the path-finding task in the bAbI dataset, which has a maximum length of two. For the inference task (Fig. 2c), we predefined 400 'relation' labels that stood as abbreviations for sequences of up to five connected edge labels. A query consisted of an incomplete triple specifying a start node and a relation label, and the required answer was the final node after following the relation sequence. Because the relation sequences were never presented to the network, they had to be inferred from the queries and targets.

As a benchmark we again compared DNCs with LSTM. In this case, the best LSTM network we found in an extensive hyper-parameter search failed to complete the first level of its training curriculum of even the easiest task (traversal), reaching an average of only 37% accuracy after almost two million training examples; DNCs reached an average of 98.8% accuracy on the final lesson of the same curriculum after around one million training examples.

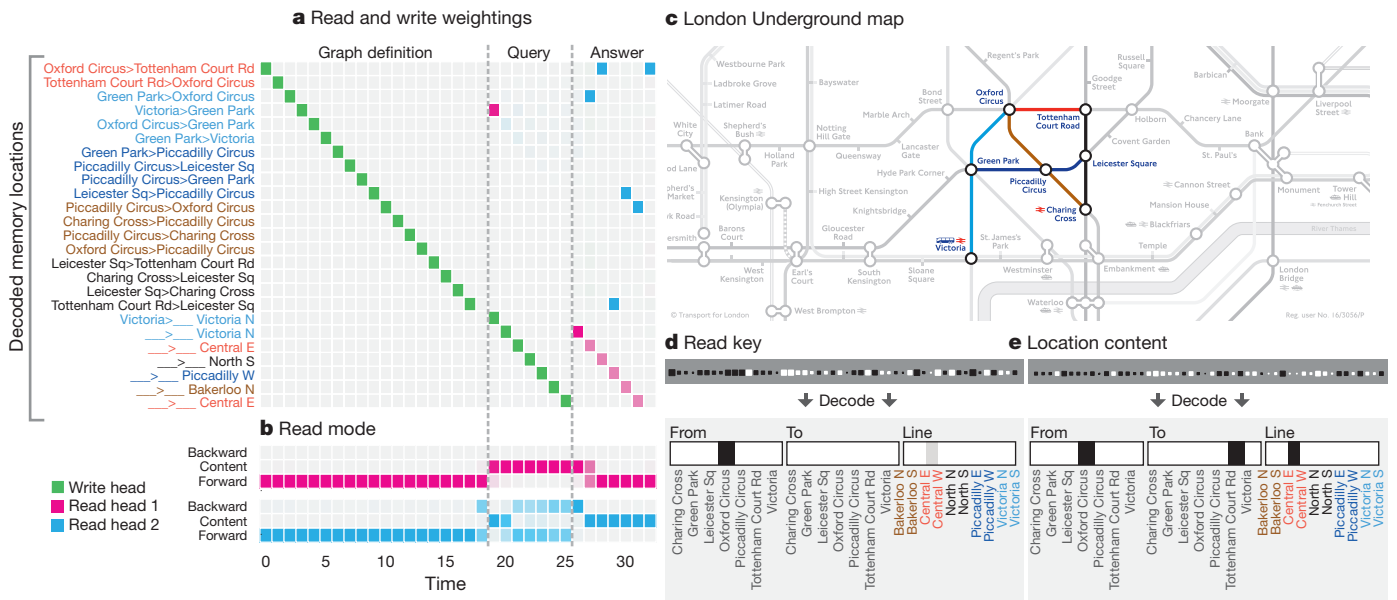
Figure 3 illustrates a DNC's use of memory allocation, content lookup and temporal linkage to store and traverse the London Underground map. Visualization of a DNC trained on shortest-path suggests that it progressively explored the links radiating out from the start and end nodes until a connecting path was found (Supplementary Video 1).

### Block puzzle experiments

Next we wanted to investigate the ability of DNCs to exploit their memory for logical planning tasks. To do this, we created a block puzzle game inspired by Winograd's SHRDLU<sup>30</sup>—a classic artificial intelligence demonstration of an environment with movable objects and a rule-based agent that executed user instructions. Unlike the previous experiments, for which the networks were trained with supervised learning, we applied a form of reinforcement learning in which a sequence of instructions describing a goal is coupled to a reward function that evaluates whether the goal is satisfied—a set-up that resembles an animal training protocol with a symbolic task cue<sup>31</sup>.

Our environment, which we term Mini-SHRDLU, contains a set of numbered blocks on a grid board. An agent, given a view of the board as input, can move the top block from a column and deposit it on top of a stack in another column. At every episode, we generated a start board configuration and several possible goals. Each goal, identified by a single-letter label, was composed of several individual constraints on adjacent block pairs that were transmitted one constraint per time-step (goal "T" is "block 6 below 2; block 4 left of 1; ...") (Fig. 4b, c). After all of the goals were presented, a single goal label was chosen at random, and the agent was cued to satisfy that goal.

The DNC used its memory to store the instructions by iteratively writing goals to locations (Fig. 4a); it could then carry out any chosen goal (Fig. 4c, Supplementary Video 2). We observed that, at the time a goal was written, but many steps before execution was required, the first action could be decoded from memory (Fig. 4d). This indicates that the DNC had written its decision to memory before acting upon it; thus, remarkably,

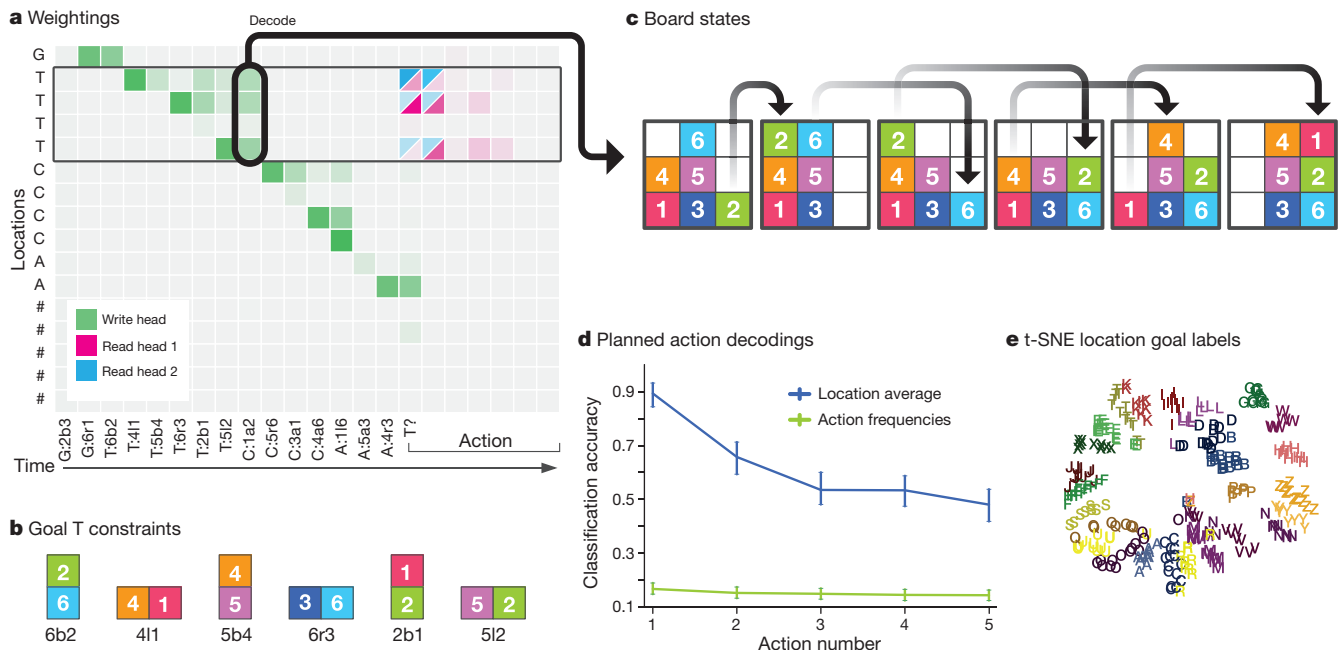


**Figure 3 | Traversal on the London Underground.** **a**, During the graph definition phase, the network writes each triple in the map to a separate memory location, as shown by the write weightings (green). During the query phase, the start station (Victoria) and lines to be traversed are recorded. The triple stored in each location can be recovered by a logistic regression decoder, as shown on the vertical axis. **b**, The read mode distribution during the answer phase reveals that read head 1 (pink)

follows temporal links forwards to retrieve the instructions in order, whereas read head 2 (blue) uses content lookup to find the stations along the path. The degree of coloration indicates how strongly each mode is used. **c**, The region of the map used. **d**, The final content key used by read head 2 is decoded as a triple with no destination. **e**, The memory location returned by the key contains the complete triple, allowing the network to infer the destination (Tottenham Court Rd).

DNC learned to make a plan. As with the graph tasks, learning followed a curriculum that gradually increased the number of blocks on the board and constraints in a goal as well as the number of goals and

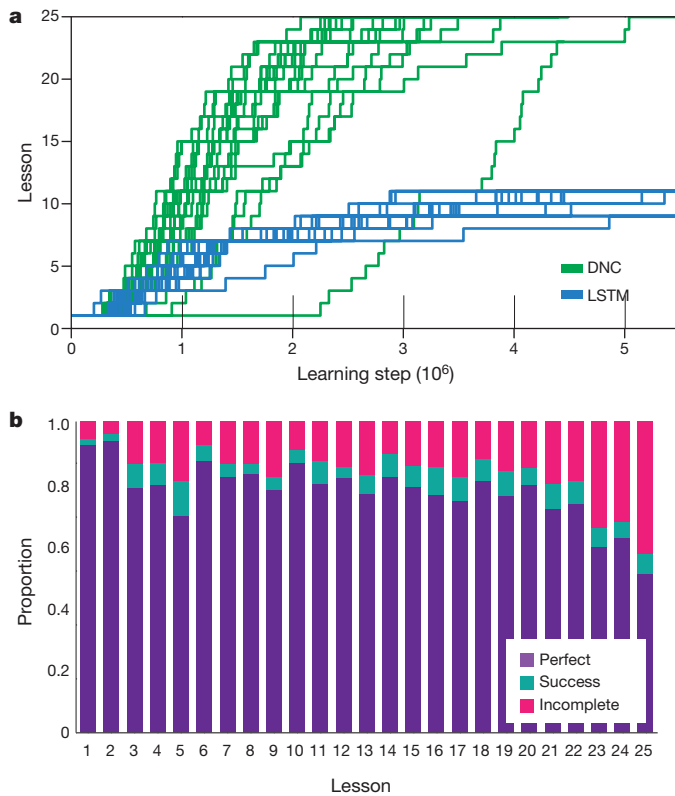
the minimum number of actions needed to find a solution (Methods). Again, the DNC performed substantially better than LSTM (see Fig. 5 and Extended Data Fig. 3).



**Figure 4 | Mini-SHRDLU analysis.** **a**, In a short example episode, the network wrote goal-related information to sequences of memory locations. ('G', 'T', 'C' and 'A' denote goals; the numbers refer to the block number; 'b', 'a', 'l' and 'r' denote 'below', 'above', 'left of' and 'right of', respectively.) The chosen goal was T ('T?'), and the read heads focused on the locations containing goal T. **b**, The constraints comprising goal T. **c**, The policy made an optimal sequence of moves to satisfy its constraints. **d**, On 800 random episodes, the first five actions that the network took for the chosen goal were decoded from memory using logistic regression at the time-step after the goal was written (box in **a** with arrow to **c**). Decoding accuracy

for the first action is 89%, compared to 17% using action frequencies alone, indicating that the network had determined a plan at the time of writing, many steps before execution. Error bars represent 5–95 percentile bootstrapped confidence intervals on validation data. **e**, Within trials, we average the location contents associated with each goal label into single vectors. Across trials, we create a dataset of these vectors and perform t-SNE (t-distributed stochastic neighbour embedding) dimensionality reduction down to two dimensions. This shows that each goal label is coded geometrically in the memory locations.





**Figure 5 | Mini-SHRDLU results.** **a**, 20 replicated training runs with different random-number seeds for a DNC and LSTM. Only the DNC was able to complete the learning curriculum. **b**, A single DNC was able to solve a large percentage of problems optimally from each previous lesson (perfect), with a few episodes solved in extra moves (success), and some failures to satisfy all constraints (incomplete).

## Discussion

Taken together, the bAbI and graph tasks demonstrate that DNCs are able to process and reason about graph-structured data regardless of whether the links are implicit or explicit. Moreover, we have seen that the structure of the data source is directly reflected in the memory-access procedures learned by the controller. The Mini-SHRDLU problem shows that a systematic use of memory also emerges when a DNC learns by reinforcement to act in pursuit of a set of symbolic goals.

The theme connecting these tasks is the need to learn to represent and reason about the complex, quasi-regular structure embedded in data sequences. In each problem, domain regularities, such as the conventions for representing graphs, are invariant across all sequences shown; on the other hand, for any given sequence, a DNC must detect and capture novel variability as episodic variables in memory. This mixture of large-scale structure and microscopic variability is generic to many problems that confront a cognitive agent<sup>32–34</sup>. For example, in visual scenes, stories and action plans, broad regularities bind together novel variation in any exemplar. Rooms statistically have chairs in them, but the shape and location of a particular chair in a room are variables. These variable values can be written to the external memory of a DNC, leaving the controller network free to concentrate on learning global regularities.

Our experiments focused on relatively small-scale synthetic tasks, which have the advantage of being easy to generate and interpret. For such problems, memory matrices of up to 512 locations were sufficient. To tackle real-world data we will need to scale up to thousands or millions of locations, at which point the memory will be able to store more information than can be contained in the weights of the controller. Such systems should be able to continually acquire knowledge through exposure to large, naturalistic data sources, even without

adapting network parameters. We aim to further develop DNCs to serve as representational engines for one-shot learning<sup>35–37</sup>, scene understanding<sup>38</sup>, language processing<sup>39</sup> and cognitive mapping<sup>40</sup>, capable of intuiting the variable structure and scale of the world within a single, generic model.

**Online Content** Methods, along with any additional Extended Data display items and Source Data, are available in the online version of the paper; references unique to these sections appear only in the online paper.

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**Supplementary Information** is available in the online version of the paper.

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## METHODS

A glossary of symbols and full equations for the DNC model are provided in Supplementary Information.

**Controller network.** At every time-step  $t$  the controller network  $\mathcal{N}$  receives an input vector  $\mathbf{x}_t \in \mathbb{R}^X$  from the dataset or environment and emits an output vector  $\mathbf{y}_t \in \mathbb{R}^Y$  that parameterizes either a predictive distribution for a target vector  $\mathbf{z}_t \in \mathbb{R}^Y$  (supervised learning) or an action distribution (reinforcement learning). Additionally, the controller receives a set of  $R$  read vectors  $\mathbf{r}_{t-1}^1, \dots, \mathbf{r}_{t-1}^R$  from the memory matrix  $M_{t-1} \in \mathbb{R}^{N \times W}$  at the previous time-step, via the read heads. It then emits an interface vector  $\xi_t$  that defines its interactions with the memory at the current time-step. For notational convenience, we concatenate the read and input vectors to obtain a single controller input vector  $\chi_t = [\mathbf{x}_t; \mathbf{r}_{t-1}^1; \dots; \mathbf{r}_{t-1}^R]$ . Any neural network can be used for the controller, but we have used the following variant of the deep LSTM architecture<sup>41</sup>:

$$\begin{aligned} i_t^l &= \sigma(W_i^l[\chi_t; \mathbf{h}_{t-1}^l; \mathbf{h}_t^{l-1}] + \mathbf{b}_i^l) \\ f_t^l &= \sigma(W_f^l[\chi_t; \mathbf{h}_{t-1}^l; \mathbf{h}_t^{l-1}] + \mathbf{b}_f^l) \\ s_t^l &= f_t^l s_{t-1}^l + i_t^l \tanh(W_s^l[\chi_t; \mathbf{h}_{t-1}^l; \mathbf{h}_t^{l-1}] + \mathbf{b}_s^l) \\ o_t^l &= \sigma(W_o^l[\chi_t; \mathbf{h}_{t-1}^l; \mathbf{h}_t^{l-1}] + \mathbf{b}_o^l) \\ \mathbf{h}_t^l &= o_t^l \tanh(s_t^l) \end{aligned}$$

where  $l$  is the layer index,  $\sigma(x) = 1/(1 + \exp(-x))$  is the logistic sigmoid function,  $\mathbf{h}_t^l, i_t^l, f_t^l, s_t^l$  and  $o_t^l$  are the hidden, input gate, forget gate, state and output gate activation vectors, respectively, of layer  $l$  at time  $t$ .  $\mathbf{h}_t^0 = \mathbf{0}$  for all  $t$ ;  $\mathbf{h}_0^l = \mathbf{s}_0^l = \mathbf{0}$  for all  $l$ . The  $W$  terms denote learnable weight matrices (for example,  $W_i^l$  is the matrix of weights going into the layer- $l$  input gates) and the  $\mathbf{b}$  terms are learnable biases.

At each time-step, the controller emits an output vector  $\mathbf{v}_t$  and an interface vector  $\xi_t \in \mathbb{R}^{(W \times R) + 3W + 5R + 3}$ , defined as

$$\begin{aligned} \mathbf{v}_t &= W_v[\mathbf{h}_t^1; \dots; \mathbf{h}_t^L] \\ \xi_t &= W_\xi[\mathbf{h}_t^1; \dots; \mathbf{h}_t^L] \end{aligned}$$

Assuming the controller network is recurrent, its outputs are a function of the complete history  $(\chi_1, \dots, \chi_t)$  of its inputs up to the current time-step. We can therefore encapsulate the operation of the controller as

$$(\mathbf{v}_t, \xi_t) = \mathcal{N}([\chi_1; \dots; \chi_t]; \theta)$$

where  $\theta$  is the set of trainable network weights. It is also possible to use a feedforward controller, in which case  $\mathcal{N}$  is a function of  $\chi_t$  only; however, we use only recurrent controllers in this paper. Finally, the output vector  $\mathbf{y}_t$  is defined by adding  $\mathbf{v}_t$  to a vector obtained by passing the concatenation of the current read vectors through the  $RW \times Y$  weight matrix  $W_r$

$$\mathbf{y}_t = \mathbf{v}_t + W_r[\mathbf{r}_t^1; \dots; \mathbf{r}_t^R]$$

This arrangement allows the DNC to condition its output decisions on memory that has just been read; it would not be possible to pass this information back to the controller, and thereby use them to determine  $\mathbf{v}_t$ , without creating a cycle in the computation graph.

**Interface parameters.** Before being used to parameterize the memory interactions, the interface vector  $\xi_t$  is subdivided as follows:

$$\xi_t = \left[ \mathbf{k}_t^{r,1}; \dots; \mathbf{k}_t^{r,R}; \hat{\beta}_t^{r,1}; \dots; \hat{\beta}_t^{r,R}; \mathbf{k}_t^w; \hat{\beta}_t^w; \hat{\mathbf{e}}_t; \mathbf{v}_t; \hat{f}_t^1; \dots; \hat{f}_t^R; \hat{\mathbf{s}}_t^a; \hat{\mathbf{s}}_t^w; \hat{\pi}_t^1; \dots; \hat{\pi}_t^R \right]$$

The individual components are then processed with various functions to ensure that they lie in the correct domain. The logistic sigmoid function is used to constrain to  $[0, 1]$ . The ‘oneplus’ function is used to constrain to  $[1, \infty)$ , where

$$\text{oneplus}(x) = 1 + \log(1 + e^x)$$

and the softmax function is used to constrain vectors to  $\mathcal{S}_N$ , the  $N - 1$ -dimensional unit simplex

$$\mathcal{S}_N = \left\{ \alpha \in \mathbb{R}^N : \alpha_i \in [0, 1], \sum_{i=1}^N \alpha_i = 1 \right\}$$

After processing we have the following set of scalars and vectors:

- $R$  read keys  $\{\mathbf{k}_t^{r,i} \in \mathbb{R}^W; 1 \leq i \leq R\}$ ;
- $R$  read strengths  $\{\beta_t^{r,i} = \text{oneplus}(\hat{\beta}_t^{r,i}) \in [1, \infty); 1 \leq i \leq R\}$ ;

- the write key  $\mathbf{k}_t^w \in \mathbb{R}^W$ ;
- the write strength  $\beta_t^w = \text{oneplus}(\hat{\beta}_t^w) \in [1, \infty)$ ;
- the erase vector  $\mathbf{e}_t = \sigma(\hat{\mathbf{e}}_t) \in [0, 1]^W$ ;
- the write vector  $\mathbf{v}_t \in \mathbb{R}^W$ ;
- $R$  free gates  $\{f_t^i = \sigma(\hat{f}_t^i) \in [0, 1]; 1 \leq i \leq R\}$ ;
- the allocation gate  $g_t^a = \sigma(\hat{g}_t^a) \in [0, 1]$ ;
- the write gate  $g_t^w = \sigma(\hat{g}_t^w) \in [0, 1]$ ; and
- $R$  read modes  $\{\pi_t^i = \text{softmax}(\hat{\pi}_t^i) \in \mathcal{S}_3; 1 \leq i \leq R\}$ .

The use and interpretation of these terms will be explored in the following sections.

**Reading and writing to memory.** Selecting locations for reading and writing depends on weightings, which are vectors of non-negative numbers whose elements sum to at most 1. The complete set of allowed weightings over  $N$  locations is the non-negative orthant of  $\mathbb{R}^N$  with the unit simplex as a boundary (known as the ‘corner of the cube’):

$$\Delta_N = \left\{ \alpha \in \mathbb{R}^N : \alpha_i \in [0, 1], \sum_{i=1}^N \alpha_i \leq 1 \right\}$$

For the read operation,  $R$  read weightings  $\{\mathbf{w}_t^{r,1}, \dots, \mathbf{w}_t^{r,R} \in \Delta_N\}$  are used to compute weighted averages of the contents of the locations, thereby defining the read vectors  $\{\mathbf{r}_t^1, \dots, \mathbf{r}_t^R\}$  as

$$\mathbf{r}_t^i = M_t^\top \mathbf{w}_t^{r,i}$$

The read vectors are appended to the controller input at the next time-step, giving it access to the memory contents. The write operation is mediated by a single write weighting  $\mathbf{w}_t^w \in \Delta_N$ , which is used in conjunction with an erase vector  $\mathbf{e}_t \in [0, 1]^W$  and a write vector  $\mathbf{v}_t \in \mathbb{R}^W$  (both emitted by the controller) to modify the memory as follows:

$$M_t = M_{t-1} \circ (E - \mathbf{w}_t^w \mathbf{e}_t^\top) + \mathbf{w}_t^w \mathbf{v}_t^\top$$

where  $\circ$  denotes element-wise multiplication and  $E$  is an  $N \times W$  matrix of ones. The computation of the read and write weightings is detailed in the following section.

**Memory addressing.** The system uses a combination of content-based addressing and dynamic memory allocation to determine where to write in memory, and a combination of content-based addressing and temporal memory linkage to determine where to read. These mechanisms, all of which are parameterized by the interface vectors defined in Methods section ‘Interface parameters’, are described in detail below.

**Content-based addressing.** All content lookup operations on the memory  $M \in \mathbb{R}^{N \times W}$  use the following function

$$\mathcal{C}(M, \mathbf{k}, \beta)[i] = \frac{\exp\{\mathcal{D}(\mathbf{k}, M[i, \cdot])\beta\}}{\sum_j \exp\{\mathcal{D}(\mathbf{k}, M[j, \cdot])\beta\}}$$

where  $\mathbf{k} \in \mathbb{R}^W$  is a lookup key,  $\beta \in [1, \infty)$  is a scalar representing key strength and  $\mathcal{D}$  is the cosine similarity:

$$\mathcal{D}(\mathbf{u}, \mathbf{v}) = \frac{\mathbf{u} \cdot \mathbf{v}}{\|\mathbf{u}\| \|\mathbf{v}\|}$$

The weighting  $\mathcal{C}(M, \mathbf{k}, \beta) \in \mathcal{S}_N$  defines a normalized probability distribution over the memory locations. In later sections, we will encounter weightings in  $\Delta_N$  that may sum to less than one, with the missing weight implicitly assigned to a null operation that does not access any of the locations. Content lookup operations are performed by both the read and write heads.

**Dynamic memory allocation.** To allow the controller to free and allocate memory as needed, we developed a differentiable analogue of the ‘free list’ memory allocation scheme<sup>42</sup>, whereby a list of available memory locations is maintained by adding to and removing addresses from a linked list. Denote by  $\mathbf{u}_t \in [0, 1]^N$  the memory usage vector at time  $t$ , and define  $\mathbf{u}_0 = \mathbf{0}$ . Before writing to memory, the controller emits a set of free gates  $f_t^i$ , one per read head, that determine whether

the most recently read locations can be freed. The memory retention vector  $\psi_t \in [0, 1]^N$  represents by how much each location will not be freed by the free gates, and is defined as

$$\psi_t = \prod_{i=1}^R \left( \mathbf{1} - f_t^i \mathbf{w}_{t-1}^{r,i} \right)$$

The usage vector can then be defined as

$$\mathbf{u}_t = (\mathbf{u}_{t-1} + \mathbf{w}_{t-1}^w - \mathbf{u}_{t-1} \circ \mathbf{w}_{t-1}^w) \circ \psi_t$$

where  $\circ$  denotes element-wise multiplication. Intuitively, locations are used if they have been retained by the free gates ( $\psi_t[i] \approx 1$ ), and were either already in use or have just been written to. Every write to a location increases its usage, up to a maximum of 1, and usage can only be subsequently decreased (to a minimum of 0) using the free gates; the elements of  $\mathbf{u}_t$  are therefore bounded in the range  $[0, 1]$ . Once  $\mathbf{u}_t$  has been determined, the free list  $\phi_t \in \mathbb{Z}^N$  is defined by sorting the indices of the memory locations in ascending order of usage;  $\phi_t[1]$  is therefore the index of the least used location. The allocation weighting  $\mathbf{a}_t \in \Delta_N$ , which is used to provide new locations for writing, is

$$\mathbf{a}_t[\phi_t[j]] = (1 - \mathbf{u}_t[\phi_t[j]]) \prod_{i=1}^{j-1} \mathbf{u}_t[\phi_t[i]] \quad (1)$$

If all usages are 1, then  $\mathbf{a}_t = \mathbf{0}$  and the controller can no longer allocate memory without first freeing used locations. The sort operation induces discontinuities at the points at which the sort order changes. We ignore these discontinuities when calculating the gradient, as they do not seem to be relevant to learning.

*Write weighting.* The controller can write to newly allocated locations, or to locations addressed by content, or it can choose not to write at all. First, a write content weighting  $\mathbf{c}_t^w \in \mathcal{S}_N$  is constructed using the write key  $\mathbf{k}_t^w$  and write strength  $\beta_t^w$ :

$$\mathbf{c}_t^w = \mathcal{C}(M_{t-1}, \mathbf{k}_t^w, \beta_t^w)$$

$\mathbf{c}_t^w$  is interpolated with the allocation weighting  $\mathbf{a}_t$  defined in equation (1) to determine a write weighting  $\mathbf{w}_t^w \in \Delta_N$ :

$$\mathbf{w}_t^w = g_t^w [\beta_t^w \mathbf{a}_t + (1 - g_t^w) \mathbf{c}_t^w] \quad (2)$$

where  $g_t^a \in [0, 1]$  is the allocation gate governing the interpolation and  $g_t^w \in [0, 1]$  is the write gate. If the write gate is 0, then nothing is written, regardless of the other write parameters; it can therefore be used to protect the memory from unnecessary modifications.

*Temporal memory linkage.* The memory allocation system defined above stores no information about the order in which the memory locations are written to. However, there are many situations in which retaining this information is useful: for example, when a sequence of instructions must be recorded and retrieved in order. We therefore use a temporal link matrix  $L_t \in [0, 1]^{N \times N}$  to keep track of consecutively modified memory locations (Fig. 1d).

$L_t[i, j]$  represents the degree to which location  $i$  was the location written to after location  $j$ , and each row and column of  $L_t$  defines a weighting over locations:  $L_t[i, \cdot] \in \Delta_N$  and  $L_t[\cdot, j] \in \Delta_N$  for all  $i, j$  and  $t$ . To define  $L_t$ , we require a precedence weighting  $\mathbf{p}_t \in \Delta_N$ , where element  $\mathbf{p}_t[i]$  represents the degree to which location  $i$  was the last one written to.  $\mathbf{p}_t$  is defined by the recurrence relation

$$\begin{aligned} \mathbf{p}_0 &= \mathbf{0} \\ \mathbf{p}_t &= \left( 1 - \sum_i \mathbf{w}_t^w[i] \right) \mathbf{p}_{t-1} + \mathbf{w}_t^w \end{aligned}$$

where  $\mathbf{w}_t^w$  is the write weighting defined in equation (2). Every time a location is modified, the link matrix is updated to remove old links to and from that location. New links from the last-written location are then added. We use the following recurrence relation to implement this logic:

$$\begin{aligned} L_0[i, j] &= 0 \quad \forall i, j \\ L_t[i, i] &= 0 \quad \forall i \\ L_t[i, j] &= (1 - \mathbf{w}_t^w[i] - \mathbf{w}_t^w[j]) L_{t-1}[i, j] + \mathbf{w}_t^w[i] \mathbf{p}_{t-1}[j] \end{aligned}$$

Self-links are excluded (the diagonal of the link matrix is always 0) because it is unclear how to follow a transition from a location to itself. The rows and columns of  $L_t$  represent the weights of the temporal links going into and out from particular memory slots, respectively. Given  $L_t$ , the backward weighting  $\mathbf{b}_t^i \in \Delta_N$  and forward weighting  $\mathbf{f}_t^i \in \Delta_N$  for read head  $i$  are defined as

$$\begin{aligned} \mathbf{f}_t^i &= L_t \mathbf{w}_{t-1}^{r,i} \\ \mathbf{b}_t^i &= L_t^\top \mathbf{w}_{t-1}^{r,i} \end{aligned}$$

where  $\mathbf{w}_{t-1}^{r,i}$  is the  $i$ th read weighting from the previous time-step.

*Sparse link matrix.* The link matrix is  $N \times N$  and therefore requires  $\mathcal{O}(N^2)$  resources in both memory and computation to calculate exactly. Although tolerable for the experiments in this paper, this cost rapidly becomes prohibitive as the number of locations increases. Fortunately, the link matrix is typically very sparse and can be approximated with  $\mathcal{O}(N \log N)$  computation cost and  $\mathcal{O}(N)$  memory with no discernible loss in performance (see Extended Data Fig. 4 for an example).

For some fixed  $K$ , we first calculate sparse vectors  $\hat{\mathbf{w}}_t^w$  and  $\hat{\mathbf{p}}_{t-1}$  by sorting  $\mathbf{w}_{t-1}^w$  and  $\mathbf{p}_{t-1}$ , setting all but the  $K$  highest values to 0, and dividing the remaining  $K$  by their sum to ensure that they sum to 1. This step has  $\mathcal{O}(N \log N + K)$  computational cost to account for the sort and  $\mathcal{O}(N)$  memory cost. We then compute the sparse outer product  $\hat{\mathbf{w}}_t^w \hat{\mathbf{p}}_{t-1}^\top$ , which requires  $\mathcal{O}(K^2)$  memory and computation. Assuming the sparse link matrix  $\hat{L}_{t-1}$  from the previous time-step has at most  $NK$  non-zero elements,  $\hat{L}_t$  can be updated with  $\mathcal{O}(NK)$  cost using

$$\hat{L}_t[i, j] = (1 - \hat{\mathbf{w}}_t^w[i] - \hat{\mathbf{w}}_t^w[j]) \hat{L}_{t-1}[i, j] + \hat{\mathbf{w}}_t^w[i] \hat{\mathbf{p}}_{t-1}[j]$$

and then setting all elements of  $\hat{L}_t$  that are less than  $1/K$  to zero. Because each row and column of  $\hat{L}_t$  sums to at most 1, this operation guarantees that  $\hat{L}_t$  has at most  $K$  non-zero elements per row and column and therefore at most  $NK$  non-zero elements. Finally, the forward and backward weightings can be calculated with  $\mathcal{O}(NK)$  computation cost and  $\mathcal{O}(N)$  memory cost as follows:

$$\begin{aligned} \mathbf{f}_t^i &= \hat{L}_t \mathbf{w}_{t-1}^{r,i} \\ \mathbf{b}_t^i &= \hat{L}_t^\top \mathbf{w}_{t-1}^{r,i} \end{aligned}$$

Because  $K$  is a constant that is independent of  $N$  (in practice  $K = 8$  appears to be sufficient, regardless of memory size), the complete sparse update is  $\mathcal{O}(N \log N)$  in computation and  $\mathcal{O}(N)$  in memory.

*Read weighting.* Each read head  $i$  computes a content weighting  $\mathbf{c}_t^{r,i} \in \Delta_N$  using a read key  $\mathbf{k}_t^{r,i} \in \mathbb{R}^W$ :

$$\mathbf{c}_t^{r,i} = \mathcal{C}(M_t, \mathbf{k}_t^{r,i}, \beta_t^{r,i})$$

Each read head also receives a read mode vector  $\boldsymbol{\pi}_t^i \in \mathcal{S}_3$ , which interpolates among the backward weighting  $\mathbf{b}_t^i$ , the forward weighting  $\mathbf{f}_t^i$  and the content read weighting  $\mathbf{c}_t^{r,i}$ , thereby determining the read weighting  $\mathbf{w}_t^{r,i} \in \mathcal{S}_3$ :

$$\mathbf{w}_t^{r,i} = \boldsymbol{\pi}_t^i[1] \mathbf{b}_t^i + \boldsymbol{\pi}_t^i[2] \mathbf{c}_t^{r,i} + \boldsymbol{\pi}_t^i[3] \mathbf{f}_t^i$$

If  $\boldsymbol{\pi}_t^i[2]$  dominates the read mode, then the weighting reverts to content lookup using  $\mathbf{k}_t^{r,i}$ . If  $\boldsymbol{\pi}_t^i[3]$  dominates, then the read head iterates through memory locations in the order they were written, ignoring the read key. If  $\boldsymbol{\pi}_t^i[1]$  dominates, then the read head iterates in the reverse order.

**Comparison with the neural Turing machine.** The neural Turing machine<sup>16</sup> (NTM) was the predecessor to the DNC described in this work. It used a similar architecture of neural network controller with read-write access to a memory matrix, but differed in the access mechanism used to interface with the memory. In the NTM, content-based addressing was combined with location-based addressing to allow the network to iterate through memory locations in order of their indices (for example, location  $n$  followed by  $n + 1$  and so on). This allowed the network to store and retrieve temporal sequences in contiguous blocks of memory. However, there were several drawbacks. First, the NTM has no mechanism to ensure that blocks of allocated memory do not overlap and interfere—a basic problem of computer memory management. Interference is not an issue for the dynamic memory allocation used by DNCs, which provides single free locations at a time, irrespective of index, and therefore does not require contiguous blocks. Second, the NTM has no way of freeing locations that have already been written to and, hence, no way of reusing memory when processing long sequences. This problem is addressed in DNCs by the free gates used for de-allocation. Third, sequential information is preserved only as long as the NTM continues to iterate through consecutive locations; as soon as the write head jumps to a different part of the memory (using content-based addressing) the order of writes before and after the jump cannot be recovered by the read head. The temporal link matrix used by DNCs does not suffer from this problem because it tracks the order in which writes were made.

**bAbI task descriptions.** The bAbI dataset<sup>26</sup> comprises a set of 20 synthetic question answering tasks that are designed to test different aspects of logical



reasoning. Because the bAbI data are programmatically generated, and the code is publicly available, multiple versions of the data can be used. For our experiments we used the en-10k subset of the data available for download from [http://www.thespermwhale.com/jaseweston/babi/tasks\\_1-20\\_v1-2.tar.gz](http://www.thespermwhale.com/jaseweston/babi/tasks_1-20_v1-2.tar.gz). For each of the 20 tasks, the data comes partitioned into a training set with 10,000 questions and a test set with 1,000 questions<sup>21</sup>. The bAbI tasks are designed around stories that may contain more than one question; we treated each story as a separate sequence and presented it to the network in the form of word vectors, one word at a time. After removing all numbers, splitting the remaining text into words, and converting all words to lower case, there were 156 unique words in the lexicon and three punctuation symbols: ‘.’, ‘?’ and ‘-’, the last of which we added to indicate points in the input sequence where outputs were required. Each word was therefore represented as a size-159 one-hot vector, and the network outputs were size-159 softmax distributions. The sentences were separated by full stop characters, and all questions were delimited by a question mark followed by as many dash characters as there were words in the answer to that question. For example, a story from the ‘Counting and Lists/Sets’ task containing five questions was presented as the following input sequence of 69 word tokens:

mary journeyed to the kitchen. mary moved to the bedroom. john went back to the hallway. john picked up the milk there. what is john carrying ?  
- john travelled to the garden. john journeyed to the bedroom. what is john carrying ? - mary travelled to the bathroom. john took the apple there. what is john carrying ? - -

The answers required at the ‘-’ symbols, grouped by question into braces, are  
{milk}, {milk}, {milk apple}

The network was trained to minimize the cross-entropy of the softmax outputs with respect to the target words; the outputs during time-steps when no target was present were ignored. For each step where a target was present, the most probable word in the network’s output distribution was selected as its answer. The network was considered to have correctly replied to a question only if it got all of the target words correct (for example, it had to answer “milk” then “apple” to get the final question in the above story right). Following previous work, we evaluated our networks using the per-task ‘question error rate’ (the fraction of incorrectly answered questions).

For each task, we removed approximately 10% of the stories and added them to a validation set. All of the remaining stories were gathered together into a single training set from which a random story was drawn for each training sample. No distinction was drawn between the different tasks during training, and no explicit information was provided to the network to indicate which task the current story was drawn from. We performed a grid search over experimental hyper-parameters for all three architectures, and kept the two settings that returned (1) the lowest average question error rate over the validation set and (2) the single network with the lowest validation question error rate. We also used the validation error rate as the early stopping criterion during training (although in practice we did not observe a substantial increase in validation error due to overfitting for any of the networks).

Using word tokens led to much longer sequences than previous work on bAbI, for which sentence embeddings were used as input<sup>21,26</sup>. The distinction is notable in that it places greater stress on the long-range memory capacity of the models, and in that the word-level approach is easier to generalize to natural language, which has far greater variability in sentence length and structure than the bAbI data.

For complete results and hyper-parameters on all the bAbI tasks for DNC, NTM and LSTM, see Extended Data Tables 1 and 2.

**Graph task descriptions.** The graph tasks were supervised learning problems with each training example consisting of an input vector sequence and corresponding target vector sequence. Each vector encoded a triple consisting of a source label, an edge label and a destination label. All labels were represented as numbers between 0 and 999, with each digit represented as a 10-way one-hot encoding. We reserved a special ‘blank’ label, represented by the all-zero vector for the three digits, to indicate an unspecified label. Each label required 30 input elements, and each triple required 90. The sequences were divided into multiple phases: first a graph description phase, then a series of query and answer phases; in some cases the query and answer were separated by an additional planning phase with no input, during which the network was given time to compute the answer. During the graph description phase, the triples defining the input graph were presented in random order. Target vectors were present during only the answer phases. The input vectors had additional binary channels alongside the triples to indicate when transitions between the different phases occurred, and when a prediction was required of the network (this channel remained active throughout the answer phase). In total, the input vectors were size 92 and the target vectors

were size 90. The graph networks had 90 output units, corresponding to nine separate softmax distributions over the ten digits. The log-probability of correctly predicting an entire target triple was therefore the sum of the log-probabilities of correctly classifying each of the nine digits. Given input sequence  $\mathbf{x}$ , network output sequence  $\mathbf{y}$  and target sequence  $\mathbf{z}$ , all of length  $T$ , this yields the following cross-entropy loss function:

$$\mathcal{L}(\mathbf{x}, \mathbf{z}) = - \sum_{t=1}^T \left\{ A(t) \sum_{d=0}^9 \log[\Pr(\mathbf{z}_t^d | \mathbf{y}_t^d)] \right\}$$

where  $\mathbf{z}_t^d$  is the target at time  $t$  for digit  $d$ ,  $\mathbf{y}_t^d$  is the softmax distribution over digit  $d$  returned by the network at time  $t$ , and  $A(t)$  is an indicator function whose value was 1 during answer phases (that is, when predictions were required of the network) and 0 otherwise.

The network’s predictions were determined by taking the mode of the output distribution; the network indicated that it had completed an answer by outputting a specially reserved termination pattern. For all tasks apart from shortest-path task, performance was evaluated as the fraction of sequences for which all target vectors were correctly predicted. The metric used for the shortest-path task is described in Methods section ‘Structured prediction’.

**Random graph generation.** For all graph tasks, the graphs used to train the networks were generated by uniformly sampling a set of two-dimensional points from a unit square, each point corresponding to a node in the graph. For each node, the  $K$  nearest neighbours in the square were used as the  $K$  outbound connections, with  $K$  independently sampled from a uniform range for each node. The numerical labels for the nodes were chosen uniformly from the range [0, 999]. For the shortest-path and traversal problems, the edge labels were unique per outbound node, but non-unique across the graph. This meant that the network had to search the graph for a node and edge label simultaneously to pinpoint a particular triple, which made following paths much more difficult than if it had to search for only an edge. For a graph with  $N$  nodes,  $N$  unique numbers in the range [0, 999] were initially drawn. Then, the outbound edge labels for each node were chosen at random from those  $N$  numbers. The edge labelling procedure for the inference task is described below.

**Traversal.** A path on the graph was defined on the basis of a random walk from a random start node. At the query phase, the first input to the network was an incomplete triple with the destination unspecified (source label, edge label, \_). The input triples for the rest of the query contained only edge labels, with source and destination unspecified. During the answer phase, no inputs were presented and the target output was the sequence of complete triples along the path. To succeed, the network had to infer the destination of each triple, and remember it as the implicit source for the next triple.

**Shortest path.** In the query phase, a single incomplete triple was presented, defining the start and end nodes (source, \_, destination). Each query was followed by a 10-time-step planning phase, allowing the network to perform computations and to attempt to determine a shortest path. During the answer phase, the network emitted a sequence of triples corresponding to a path. Unlike the traversal task, it also received input triples during the answer phase, indicating the actions chosen on the previous time-step. This makes the problem a ‘structured prediction’ problem, which we explain further in Methods section ‘Structured prediction’. As described therein, the input triples were sometimes the network’s own predictions from the previous time-step, and during training were sometimes provided by an optimal planner recalculating a shortest path to the end node. To ensure that the network always moved to a valid node, the output distribution was renormalized over the set of possible triples outgoing from the current node. The performance measure was the fraction of sequences for which the network found a minimally short path.

**Inference.** We define a ‘relation’ to be a concatenation of two or more edge labels that is given a distinct label; the label therefore acts as a kind of alias for the sequence. For the inference task, numbers from 0 to 9 indicated single edge labels and numbers from 10 to 410 indicated relation labels. The relations were generated as unique sequences of single edges of length 2–5, with 100 distinct sequences for each length. The sequences and labels were fixed for all networks trained on the task. During the query phase, an incomplete triple was presented, consisting of a start node and a relation label (start node, relation label, \_). This was followed by a 10-time-step planning phase. The single target vector during the answer phase was the completed triple from the query: (start node, relation label, end node). To solve the problem the network had to infer the relation sequence from its label and perform an implicit traversal along that sequence during the planning phase to reach the destination. The relations were never passed as input the network, so it had to infer them from error signals alone.

**Structured prediction.** The shortest-path task can be considered a structured prediction problem<sup>43,44</sup>, because the output decisions of the network determine the sequence of nodes traversed on the graph and, hence, influence the future decisions of network and the prediction costs. To bring nomenclature in line with the literature on this topic, we refer to the output distribution of the network as a policy  $\pi(a | s)$  over the actions  $a$  available to the network in state  $s$ . The state incorporates both the node currently occupied by the network and the latent state of the network itself. The actions are the outgoing edges from the current node (recall that the output distribution is renormalized over the allowed triples after each move). Following policy  $\pi$ , the induced distribution over states at time-step  $t$  is denoted  $\rho_t^*(s)$ . We denote the optimal policy as  $\pi^*(a | s)$  with corresponding state distribution  $\rho_t^*(s)$ . The conventional supervised loss is

$$J^{\text{sup}}(\pi) = \sum_{t=1}^T E_{s_t \sim \rho_t^*(s)} [l[\pi^*(\cdot | s_t), \pi(\cdot | s_t)]]$$

where  $l[\pi^*(\cdot | s_t), \pi(\cdot | s_t)] = -\sum_a \pi^*(a | s_t) \log [\pi(a | s_t)]$  is the cross-entropy loss.  $\pi^*(a | s)$  is a delta function on the action that corresponds to the first step along one possible shortest path from the current node to the destination (there may be more than one). If the state distributions  $\rho_t^*(s)$  and  $\rho_t^\pi(s)$  are dissimilar, then minimizing the supervised loss function does not necessarily transfer to the true task objective

$$J^{\text{task}}(\pi) = \sum_{t=1}^T E_{s_t \sim \rho_t^\pi(s)} [l[\pi^*(\cdot | s_t), \pi(\cdot | s_t)]]$$

where the actions are sampled from the network policy and the states are therefore drawn from  $\rho_t^\pi(s)$ . To counteract this problem, we follow a similar approach to the DAGGER algorithm<sup>43</sup>, which constructs a mixture policy  $\pi^\beta(a | s) = \beta\pi^*(a | s) + (1 - \beta)\pi(a | s)$  with parameter  $\beta$  and induced state distribution  $\rho_t^\beta(s)$ . To simplify the implementation, we used a batch size of 1 and trained by taking a stochastic gradient of

$$J^\beta(\pi) = \sum_{t=1}^T E_{s_t \sim \rho_t^\beta(s)} [l[\pi^*(\cdot | s_t), \pi(\cdot | s_t)]]$$

where the actions are sampled from the network with probability  $(1 - \beta)$  and from the optimal policy with probability  $\beta$ . To make transitions driven by the network output, all possible edges connected to a source node are assigned a probability, and the most likely edge is chosen.

**Reinforcement learning.** Most reinforcement learning problems are sequential decision problems: the environment is in state  $s$  and each action  $a$  issued by the agent causes a transition of the environment state on the basis of the environment dynamics. We consider episodic problems whereby the agent acts in the environment for  $T$  steps before the environment is reset and a new episode begins. The agent thus acts to create a time series  $s_1, a_1, s_2, a_2, s_3, a_3, \dots, s_T, a_T$ . A reward function that defines the goal of the problem is given as a function of a state and an action:  $r(s_t, a_t)$ . The goal of the agent is to maximize the total expected reward over an episode. The architecture of the reinforcement learning agent presented here contains two DNC networks: a policy network that selects an action and a value network that estimates the expected future reward given the policy network and current state.

The policy specifies a parametric mapping from state observations to a probability distribution over actions:  $a \sim \pi(\cdot | s; \theta)$ , where  $\theta$  denotes the policy parameters. The total expected reward of the policy over an episode is  $J(\pi) = E[\sum_{t=1}^T r(s_t, a_t) | \pi]$ . In the context of Mini-SHRDLU, the policy-network DNC observes the environment states by receiving an observation sequence  $o_1, o_2, \dots, o_b$ , one step at a time and conditions its action on the sequence seen so far:  $\pi(\cdot | o_1, \dots, o_b; \theta)$ . The value-network DNC tries to predict the sum of future rewards for this policy given the current history of observations:  $V^\pi(o_1, \dots, o_b; \phi)$ , where  $\phi$  comprises its parameters.

The learning algorithm for the value network is conceptually simpler than that of the policy network. The value network learns by supervised regression to predict the sum of future rewards. After a mini-batch of  $L$  episodes, the value network updates its parameters  $\phi$  using gradient descent on the loss function

$$C(\phi) = \frac{1}{2L} \sum_{l=1}^L \sum_{t=1}^T \left\| \sum_{\tau=t}^T r(s_\tau^l, a_\tau^l) - V^\pi(o_1, \dots, o_b; \phi) \right\|^2$$

The action distribution of the policy network  $\pi(a_t | o_1, \dots, o_b; \theta)$  is a softmax over a discrete set of actions. We use a policy gradient method to optimize the policy parameters<sup>45,46</sup>. After each mini-batch of  $L$  episodes, the policy parameter gradient direction to ascend  $J(\pi)$  is

$$\nabla_\theta J(\pi) \approx \frac{1}{L} \sum_{l=1}^L \sum_{t=1}^T \nabla_\theta \log[\pi(a_t^l | o_1^l, \dots, o_b^l; \theta)] \left\{ E \left[ \sum_{\tau \geq t}^T r(s_\tau^l, a_\tau^l) \middle| s_t^l, a_t^l \right] - E \left[ \sum_{\tau \geq t}^T r(s_\tau^l, a_\tau^l) \middle| s_t^l \right] \right\}$$

The quantity  $E \left[ \sum_{\tau \geq t}^T r(s_\tau^l, a_\tau^l) \middle| s_t^l, a_t^l \right] - E \left[ \sum_{\tau \geq t}^T r(s_\tau^l, a_\tau^l) \middle| s_t^l \right]$ , known as the advantage, represents the amount that the value changes from taking action  $a_t^l$  in state  $s_t^l$ . Using the value network, we can approximate the advantage using the temporal difference error

$$\delta_t^l = r(s_t^l, a_t^l) + V^\pi(o_1^l, \dots, o_b^l; \phi) - V^\pi(o_1^l, \dots, o_b^l; \phi)$$

We use a slight modification of this expression for the advantage<sup>47</sup> to reduce the bias in the value networks. The advantage is estimated using a geometric series of temporal difference errors  $\sum_{\tau \geq t} \lambda^{\tau-t} \delta_\tau^l$ , where  $\lambda$  is a parameter that controls a bias-variance trade-off between estimates based on the empirical return versus the parametric value function. Finally, the policy gradient estimate is

$$\nabla_\theta J(\pi) \approx \frac{1}{L} \sum_{l=1}^L \sum_{t=1}^T \nabla_\theta \log[\pi(a_t^l | o_1^l, \dots, o_b^l; \theta)] \sum_{\tau=t}^T \lambda^{\tau-t} \delta_\tau^l$$

**Mini-SHRDLU.** The Mini-SHRDLU board consists of an  $S \times S$  grid, each square of which is either empty or filled with a numbered block. We report experiments with  $S=3$  and a maximum of 6 blocks on the board, uniquely numbered 1–6. To generate a problem instance, we first randomly place the blocks on the board so that a block always rests on top of the highest block previously placed in its column. A sequence of  $G$  goals is generated, each composed of a number of constraints. An example of a single goal is: block 1 is below block 4; block 2 is to the right of block 5; block 3 is above block 4; and so on. Each goal represents a label for a set of constraints on the adjacency relations of the blocks. A goal can be ambiguous in that it does not describe a unique board configuration. For example, a goal consisting of only “block 1 is left of block 2” allows any configuration of the unstated blocks. Each goal is chosen by constructing a tree search of all configurations of the board that are at minimum  $D$  moves away from the starting board, randomly selecting one of these configurations, and then sampling a set of constraints on the chosen board configuration. Redundant conditions such as “block 1 is left of block 2; block 2 is right of block 1” are pruned, as are constraints that are already fulfilled by the initial state of the board.

The goals are presented sequentially to the policy network during which time the policy cannot make any moves on the board. Each constraint in the goal is presented in one time-step using a place-coded vector: (first block, adjacency relation, second block). For example, (100000, 1000, 010000) represents the constraint “block 1 is above block 2”. In addition, each constraint is labelled with the goal of which it is a part: (goal name, first block, adjacency relation, second block), where we have chosen to let the goals be 1 of 26 possible letters designated by one-hot encodings; that is, A = (1, 0, ..., 0), Z = (0, 0, ..., 1) and so on. The board is represented as a set of place-coded representations, one for each square. Therefore, (000000, 100000, ...) designates that the bottom, left-hand square is empty, block 1 is in the bottom centre square, and so on. The network also sees a binary flag that represents a ‘go cue’. While the go cue is active, a goal is selected from the list of goals that have been shown to the network, its label is retransmitted to the network for one time-step, and the network can begin to move the blocks on the board. All told, the policy observes at each time-step a vector with features (goal name, first block, adjacency relation, second block, go cue, board state). Up to 10 goals with 6 constraints each can be sent to the network before action begins.

Once the go cue arrives, it is possible for the policy network to move a block from one column to another or to pass at each turn. We parameterize these actions using another one-hot encoding so that, for a  $3 \times 3$  board, a move can be made from any column to any other; with the pass move, there are therefore 7 moves. The policy’s outputs define the probability of selecting each one of these actions, and a move is sampled at each time-step, which changes the board configuration correspondingly. The policy has a fixed number of moves to make progress on the board until the episode ends. In this setting, we can determine the minimum number of moves  $L$  required to solve the problem. We found that the early stages of learning benefited from giving the policy a number of extra steps to satisfy the instructions, in total allowing  $L + \Delta L$  moves with  $\Delta L$  fixed at 6 in the reported experiments. This parameter did not need to be fine-tuned.

The reward function for the policy equalled the number of constraints in the chosen goal that are currently satisfied minus a small cost for making invalid moves such as picking a block up from a column without any blocks. There was also a penalty for achieving the goal configuration, but then undoing it. In addition, the

policy received an extra reward that promoted higher-entropy output distributions and encouraged exploration<sup>45</sup>.

**Curriculum learning.** We found that curriculum learning<sup>28</sup> was essential for all of our experiments apart from those on the bAbi dataset. For each task, we selected a set of task parameters governing the complexity of the problem. For example, for the traversal task, these parameters were the number of nodes in the graph, the number of outgoing edges from each node and the number of steps in the traversal. We built a linear sequence of lessons in which the complexity of the task increases along at least one task parameter with each new lesson. Consistent with the observations of Zaremba and Sutskever<sup>29</sup>, we found in some tasks that performance on earlier lessons degraded if all training exemplars were drawn only from the current lesson. We followed their strategy to remedy this effect and drew 10% of exemplars uniformly from earlier lessons on the traversal, inference and Mini-SHRDLU problems. (Shortest-path lessons effectively include earlier lessons as proper subsets of the final lessons, so this is unnecessary.) After a predefined number of gradient updates, we ran a batch evaluation of the success of the network on the current problem distribution. During evaluation in each episode, on the graph problems, we deterministically sampled the most probable outputs of the network (modal sampling). For the graph problems besides shortest-path, if 90% of the episodes were solved optimally, the lesson was completed. (Shortest-path lesson completion required 80% of network paths to be no more than one step longer than the shortest path.) In the Mini-SHRDLU problem, the lesson was marked complete if 85% of the relevant goal constraints were satisfied on average over the batch. The curricula for the tasks are presented in Extended Data Tables 3–6.

**Network analysis.** In Fig. 3a, the y-axis labels are the input triples provided at each time-step, written beside the location with strongest write magnitude. The locations were re-ordered spatially on the basis of the writing order. Figure 3d, e is produced on the basis of the output of a trainer classifier, called the decoder. A logistic regression classifier was built from a dataset of 40,000 data points in which write vectors were treated as classifier inputs, with the input triples from the same time-step taken to be the classifier targets, and treating source, destination and edges as independent outputs. The digits of each element were decoded independently, so that a total of nine 10-way classifiers were used to decode each triple. The classifier was trained with an L2 regularization of the classifier weights with coefficient 0.1. Output classes that were irrelevant to the episode were excluded from the diagram. Figure 3d was produced by applying the classifier to the content lookup key; Fig. 3e was produced by applying the classifier to the contents of the memory location with the highest read weighting.

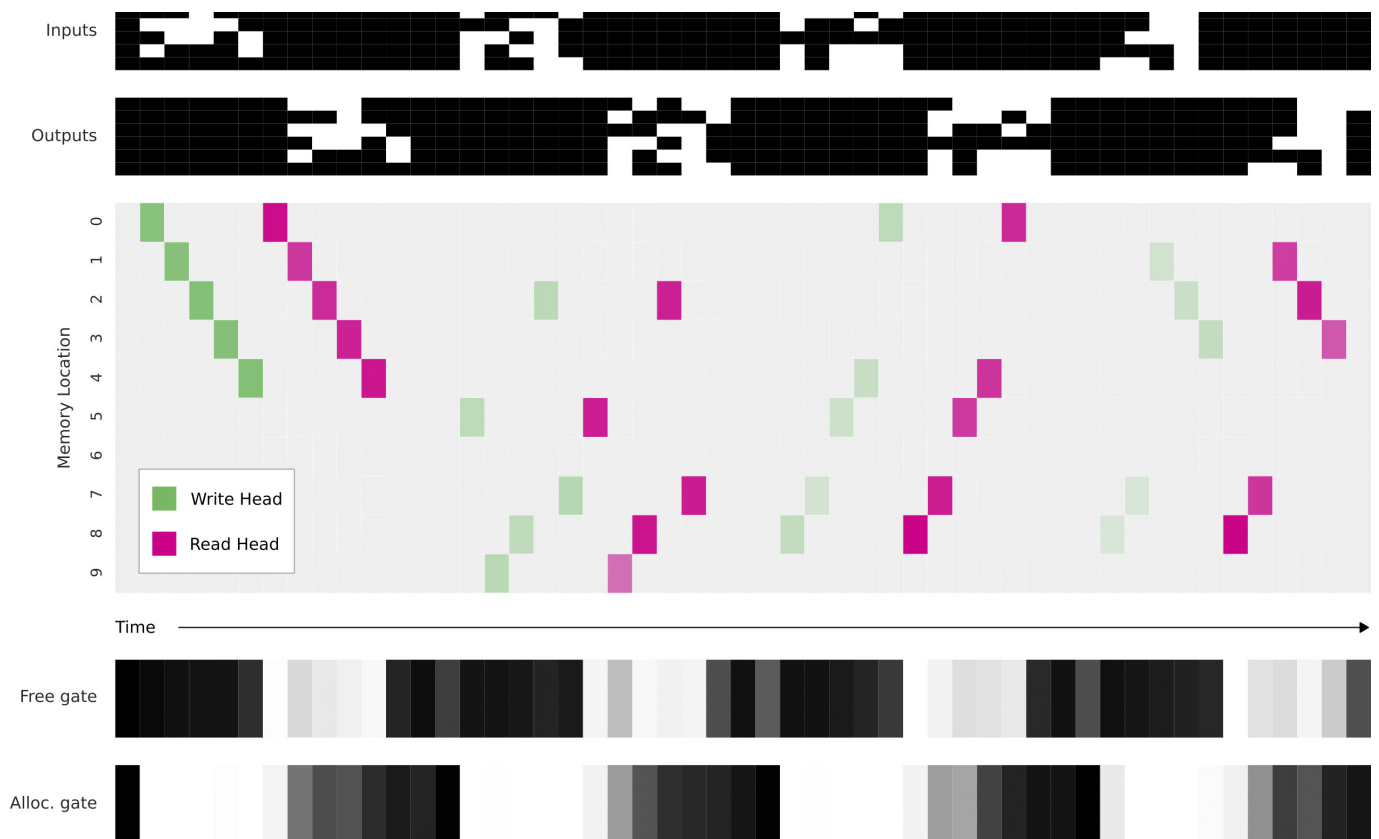
For Fig. 4d, classifiers were trained on a dataset of 800 Mini-SHRDLU episodes. On each episode, the locations to which the read heads assign more than a threshold of 0.01 weighting at the time of the query were considered relevant to the selected goal. These locations were noted, and their contents at the time when they were last written (determined by the same numerical threshold) were uniformly averaged into a single vector. The vectors therefore encapsulate an average of the locations containing the goal directly after the goal has been written to memory, but potentially many (up to about 60) time-steps before the first action occurs. The vectors were used as inputs to train the classifier to predict the first five actions following the query; that is, action 1 occurs at  $t_{\text{query}} + 0$ , action 2 occurs at  $t_{\text{query}} + 1$  and so on. The classifiers use logistic regression with an L2 regularization coefficient of 1. The action-frequencies baseline predicts each of the action choices on the basis of its frequency at that time-step after the query. Classifier accuracy is determined by constructing 100 80%/20% random splits of the episodes into

training and test data. The error bars represent 5–95 percentile accuracy on the test data partitions. In Fig. 4e, two-dimensional t-SNE<sup>48</sup> dimensionality reduction is performed on those same averaged vectors. Each data point (only half of which are shown to reduce crowding) is marked with the relevant goal label.

**Optimization.** For all experiments, results are reported on the basis of statistics gathered from 20 randomly initialized networks that share the same set of hyper-parameters. The hyper-parameters were selected from large grid searches, and are listed for each experiment in Extended Data Table 2. All networks were trained using a single machine version of Downpour SGD<sup>49</sup>. Each CPU in the machine runs a ‘worker’ process with its own copy of the model parameters. The workers generate episodes and compute gradients of the loss function with respect to the network weights using backpropagation through time<sup>50</sup>. The gradients are combined with a single central instance of the RMSProp optimization algorithm<sup>51</sup>. Once a worker computes gradients for an episode, it acquires a mutual exclusion lock guaranteeing that no other process is accessing the optimizer. The gradients are used to perform one optimization step, modifying the central master copy of the parameters, and the new parameter values are copied back to the worker. The optimizer updates a single global copy of its state, which for RMSProp includes a moving average of gradient magnitudes. Finally, the mutual exclusion is released, allowing a different worker to perform a gradient update. In the backpropagation-through-time backward pass, the gradient with respect to the LSTM controller activations was clipped element-wise to the range  $[-10, 10]$ .

**Code Availability.** A public version of the code will be made available within 6 months, linked to from our website <http://www.deepmind.com>.

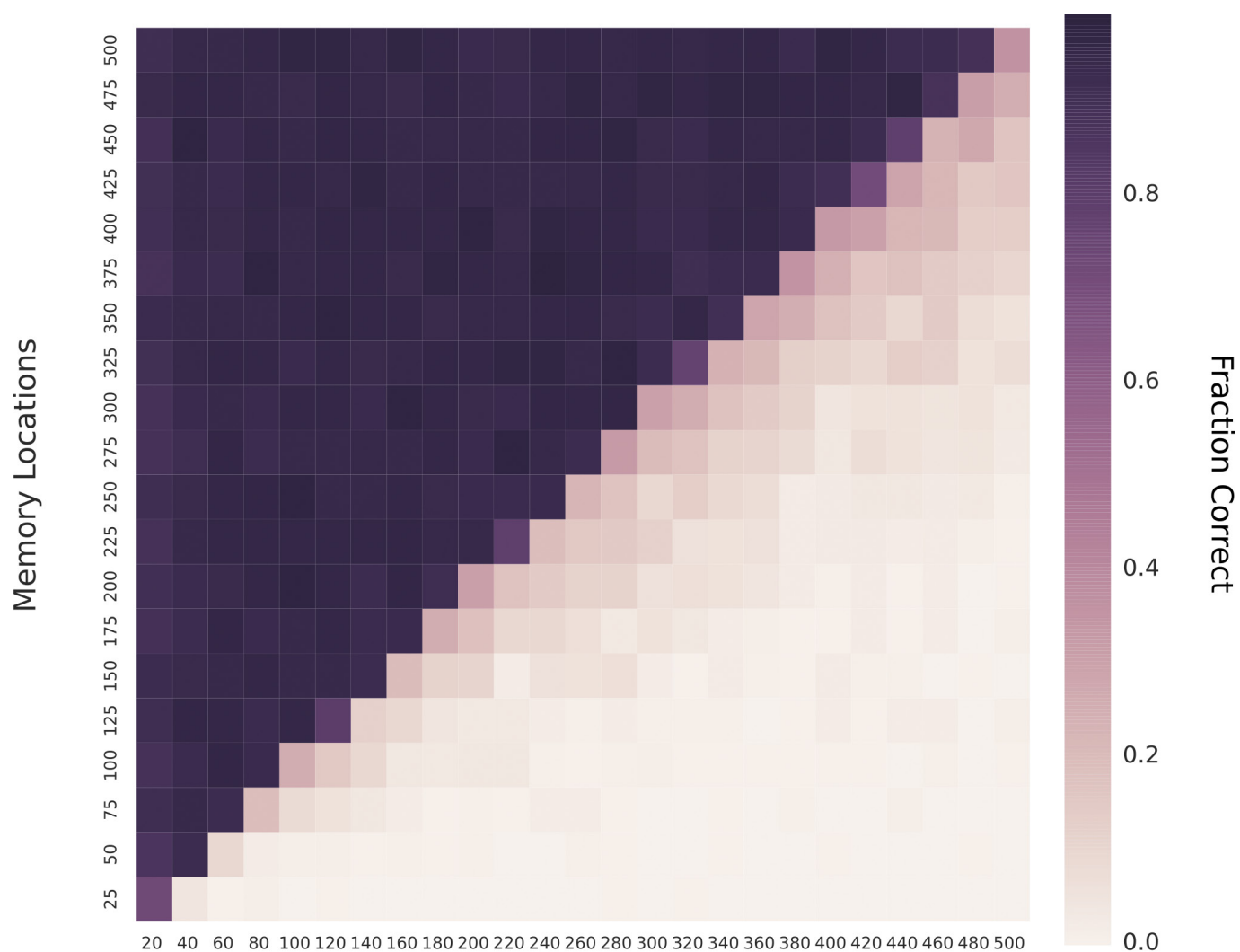
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**Extended Data Figure 1 | Dynamic memory allocation.** We trained the DNC on a copy problem, in which a series of 10 random sequences was presented as input. After each input sequence was presented, it was recreated as output. Once the output was generated, that input sequence was not needed again and could be erased from memory. We used a DNC with a feedforward controller and a memory of 10 locations—insufficient to store all 50 input vectors with no overwriting. The goal was to test

whether the memory allocation system would be used to free and re-use locations as needed. As shown by the read and write weightings, the same locations are repeatedly used. The free gate is active during the read phases, meaning that locations are deallocated immediately after they are read from. The allocation gate is active during the write phases, allowing the deallocated locations to be re-used.





## Graph Triples

**Extended Data Figure 2 | Altering the memory size of a trained network.** A DNC trained on the traversal task with 256 memory locations was tested while varying the number of memory locations and graph triples. The heat map shows the fraction of traversals of length 1–10 performed perfectly by the network, out of a batch of 100. There is a clear correspondence between the number of triples in the graph and

the number of memory locations required to solve the task, reflecting our earlier analysis (Fig. 3) that suggests that DNC writes each triple to a separate location in memory. The network appears to exploit all available memory, regardless of how much memory it was trained with. This supports our claim that memory is independent of processing in a DNC, and points to large-scale applications such as knowledge graph processing.

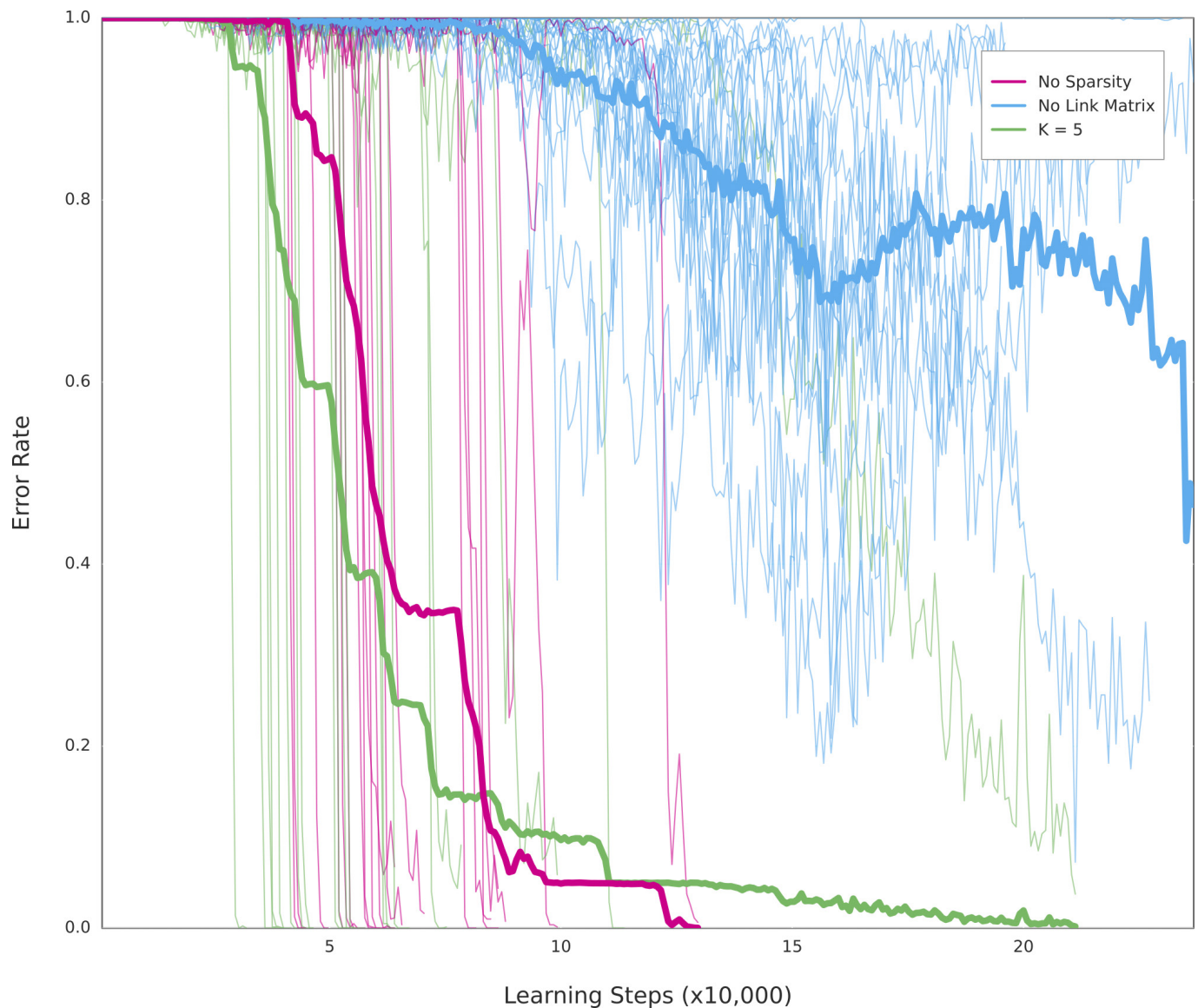
**a. DNC Percent Optimal**

Minimum Required Moves	1	77	94	95	95	93	94
	2	65	79	93	97	97	97
	3	51	63	78	85	92	94
	4	42	46	58	76	81	85
	5	39	33	46	62	72	81
	6	33	22	32	51	65	68
	7	34	17	18	30	44	50
		1	2	3	4	5	6
		Number of Constraints					

**b. LSTM Percent Optimal**

Minimum Required Moves	1	47	48	47	48	48	52
	2	39	38	34	34	31	32
	3	32	42	43	46	44	43
	4	25	22	18	14	12	14
	5	19	10	3	0.47	0	0.16
	6	20	4.7	1.1	0.16	0	0
	7	18	3	1.1	0	0	0
		1	2	3	4	5	6
		Number of Constraints					

**Extended Data Figure 3 | Probability of achieving optimal solution.** **a.** DNC. With 10 goals, the performance of a DNC network with respect to satisfying constraints in minimal time as the minimum number of moves to a goal and the number of constraints in a goal are varied. Performance was highest with a large number of constraints in each goal. **b.** The performance of an LSTM on the same test.



**Extended Data Figure 4 | Effect of link matrix sparsity on performance.** We trained the DNC on a copy problem, for which a sequence of length 1–100 of size-6 random binary vectors was given as input, and an identical sequence was then required as output. A feedforward controller was used to ensure that the sequences could not be stored in the controller state. The faint lines show error curves for 20 randomly initialized runs with identical hyper-parameters, with link matrix sparsity switched off (pink), sparsity used with  $K = 5$  (green) and with the link matrix disabled altogether (blue). The bold lines show the mean curve for each setting.

The error rate is the fraction of sequences copied with no mistakes out of a batch of 100. There does not appear to be any systematic difference between no sparsity and  $K = 5$ . We observed similar behaviour for values of  $K$  between 2 and 20 (plots omitted for clarity). The task cannot easily be solved without the link matrix because the input sequence has to be recovered in the correct order. Note the abrupt drops in error for the networks with link matrices: these are the points at which the system learns a copy algorithm that generalizes to longer sequences.

Extended Data Table 1 | bAbl best and mean results

Task	bAbl Best Results							bAbl Mean Results			
	LSTM (Joint)	NTM (Joint)	DNC1 (Joint)	DNC2 (Joint)	MemN2N (Joint) <sup>21</sup>	MemN2N (Single) <sup>21</sup>	DMN (Single) <sup>20</sup>	LSTM	NTM	DNC1	DNC2
1: 1 supporting fact	24.5	31.5	<b>0.0</b>	<b>0.0</b>	<b>0.0</b>	<b>0.0</b>	<b>0.0</b>	28.4 ± 1.5	40.6 ± 6.7	<b>9.0 ± 12.6</b>	16.2 ± 13.7
2: 2 supporting facts	53.2	54.5	1.3	0.4	1.0	<b>0.3</b>	1.8	56.0 ± 1.5	56.3 ± 1.5	<b>39.2 ± 20.5</b>	47.5 ± 17.3
3: 3 supporting facts	48.3	43.9	2.4	<b>1.8</b>	6.8	2.1	4.8	51.3 ± 1.4	47.8 ± 1.7	<b>39.6 ± 16.4</b>	44.3 ± 14.5
4: 2 argument rels.	0.4	<b>0.0</b>	<b>0.0</b>	<b>0.0</b>	<b>0.0</b>	<b>0.0</b>	<b>0.0</b>	0.8 ± 0.5	0.9 ± 0.7	<b>0.4 ± 0.7</b>	<b>0.4 ± 0.3</b>
5: 3 argument rels.	3.5	0.8	<b>0.5</b>	0.8	6.1	0.8	0.7	3.2 ± 0.5	1.9 ± 0.8	<b>1.5 ± 1.0</b>	1.9 ± 0.6
6: yes/no questions	11.5	17.1	<b>0.0</b>	<b>0.0</b>	0.1	0.1	<b>0.0</b>	15.2 ± 1.5	18.4 ± 1.6	<b>6.9 ± 7.5</b>	11.1 ± 7.1
7: counting	15.0	17.8	<b>0.2</b>	0.6	6.6	2.0	3.1	16.4 ± 1.4	19.9 ± 2.5	<b>9.8 ± 7.0</b>	15.4 ± 7.1
8: lists/sets	16.5	13.8	<b>0.1</b>	0.3	2.7	0.9	3.5	17.7 ± 1.2	18.5 ± 4.9	<b>5.5 ± 5.9</b>	10.0 ± 6.6
9: simple negation	10.5	16.4	<b>0.0</b>	0.2	<b>0.0</b>	0.3	<b>0.0</b>	15.4 ± 1.5	17.9 ± 2.0	<b>7.7 ± 8.3</b>	11.7 ± 7.4
10: indefinite knowl.	22.9	16.6	0.2	0.2	0.5	<b>0.0</b>	<b>0.0</b>	28.7 ± 1.7	25.7 ± 7.3	<b>9.6 ± 11.4</b>	14.7 ± 10.8
11: basic coreference	6.1	15.2	<b>0.0</b>	<b>0.0</b>	<b>0.0</b>	0.1	0.1	12.2 ± 3.5	24.4 ± 7.0	<b>3.3 ± 5.7</b>	7.2 ± 8.1
12: conjunction	3.8	8.9	0.1	<b>0.0</b>	0.1	<b>0.0</b>	<b>0.0</b>	5.4 ± 0.6	21.9 ± 6.6	<b>5.0 ± 6.3</b>	10.1 ± 8.1
13: compound coref.	0.5	7.4	<b>0.0</b>	0.1	<b>0.0</b>	<b>0.0</b>	0.2	7.2 ± 2.3	8.2 ± 0.8	<b>3.1 ± 3.6</b>	5.5 ± 3.4
14: time reasoning	55.3	24.2	0.3	0.4	<b>0.0</b>	0.1	<b>0.0</b>	55.9 ± 1.2	44.9 ± 13.0	<b>11.0 ± 7.5</b>	15.0 ± 7.4
15: basic deduction	44.7	47.0	<b>0.0</b>	<b>0.0</b>	0.2	<b>0.0</b>	<b>0.0</b>	47.0 ± 1.7	46.5 ± 1.6	<b>27.2 ± 20.1</b>	40.2 ± 11.1
16: basic induction	52.6	53.6	52.4	55.1	<b>0.2</b>	51.8	0.6	<b>53.3 ± 1.3</b>	53.8 ± 1.4	53.6 ± 1.9	54.7 ± 1.3
17: positional reas.	39.2	25.5	24.1	<b>12.0</b>	41.8	18.6	40.4	34.8 ± 4.1	<b>29.9 ± 5.2</b>	32.4 ± 8.0	30.9 ± 10.1
18: size reasoning	4.8	2.2	4.0	<b>0.8</b>	8.0	5.3	4.7	5.0 ± 1.4	4.5 ± 1.3	<b>4.2 ± 1.8</b>	4.3 ± 2.1
19: path finding	89.5	4.3	<b>0.1</b>	3.9	75.7	2.3	65.5	90.9 ± 1.1	86.5 ± 19.4	<b>64.6 ± 37.4</b>	75.8 ± 30.4
20: agent motiv.	1.3	1.5	<b>0.0</b>	<b>0.0</b>	<b>0.0</b>	<b>0.0</b>	<b>0.0</b>	1.3 ± 0.4	1.4 ± 0.6	<b>0.0 ± 0.1</b>	<b>0.0 ± 0.0</b>
Mean Err. (%)	25.2	20.1	4.3	<b>3.8</b>	7.5	4.2	6.4	27.3 ± 0.8	28.5 ± 2.9	<b>16.7 ± 7.6</b>	20.8 ± 7.1
Failed (err. > 5%)	15	16	<b>2</b>	<b>2</b>	6	3	<b>2</b>	17.1 ± 1.0	17.3 ± 0.7	<b>11.2 ± 5.4</b>	14.0 ± 5.0

To compare with previous results we report error rates for the single best network across all tasks (measured on the validation set) over 20 runs. The lowest error rate for each task is shown in bold. Results for MemN2N are from ref. 21; those for DMN are from ref. 20. The mean results are reported with ±s.d. for the error rates over all 20 runs for each task. The lowest mean error rate for each task is shown in bold.



**Extended Data Table 2 | Hyper-parameter settings for bAbI, graph tasks and Mini-SHRDLU**

	bAbI				Graph Tasks			Mini-SHRDLU		
	LSTM	NTM	DNC1	DNC2	Shortest Path	Traversal	Inference Tasks	Fig 4 a DNC	Fig 4 a LSTM	Figure 5 DNC
LSTM Size	512	256	256	256	$2 \times 256$	$3 \times 256$	$3 \times 256$	$2 \times 250$	$2 \times 250$	$2 \times 250$
Batch Size	1	1	1	1	1	2	32	32	32	32
Learning Rate	$1 \times 10^{-4}$	$1 \times 10^{-4}$	$1 \times 10^{-4}$	$1 \times 10^{-4}$	$3 \times 10^{-6}$	$1 \times 10^{-5}$	$1 \times 10^{-5}$	$3 \times 10^{-5}$	$3 \times 10^{-5}$	$3 \times 10^{-5}$
Memory Dimensions	–	$256 \times 64$	$256 \times 64$	$256 \times 32$	$128 \times 50$	$256 \times 50$	$128 \times 50$	$32 \times 100$	–	$32 \times 100$
Read Heads	–	4	4	8	5	5	5	3	–	2
Async. Workers	16	16	16	16	–	–	–	–	–	–
DAGGER $\beta$	–	–	–	–	0.8	–	–	–	–	–
$\lambda$	–	–	–	–	–	–	–	0.75	0.5	0.5
Entropy Cost Coeff.	–	–	–	–	–	–	–	0.5	0.5	0.5

In bAbI experiments, for all models (LSTM, NTM and DNC) we kept the hyper-parameter settings that (1) gave the lowest average validation error rate and (2) gave the single best validation error rate for a single model. For LSTM and NTM the same setting was best for both criteria, but for DNC two different settings were found (DNC1 for criterion 1 and DNC2 for criterion 2).

**Extended Data Table 3 | Curriculum results for graph traversal**

Lesson	Nodes	Out-degree	Path Length	Test	Final
1	(3, 10)	(2, 4)	(1, 1)	0.0 ± 0.0	10.7 ± 0.6
2	(3, 10)	(2, 4)	(1, 2)	0.0 ± 0.0	19.7 ± 1.3
3	(5, 10)	(2, 4)	(1, 3)	0.0 ± 0.0	27.3 ± 1.0
4	(5, 10)	(2, 4)	(1, 4)	0.0 ± 0.0	38.2 ± 2.2
5	(10, 15)	(2, 4)	(1, 4)	0.0 ± 0.0	39.7 ± 1.5
6	(10, 15)	(2, 4)	(1, 5)	0.0 ± 0.0	47.5 ± 2.2
7	(10, 20)	(2, 4)	(1, 5)	0.1 ± 0.2	48.1 ± 1.8
8	(10, 20)	(2, 4)	(1, 6)	13.6 ± 20.8	59.4 ± 5.2
9	(10, 30)	(2, 4)	(1, 6)	15.0 ± 20.3	59.0 ± 4.8
10	(10, 30)	(2, 4)	(1, 7)	72.8 ± 9.3	72.3 ± 3.5
11	(10, 30)	(2, 4)	(1, 8)	88.6 ± 5.7	81.6 ± 4.0
12	(10, 30)	(2, 4)	(1, 9)	91.8 ± 3.9	90.7 ± 3.2
13	(10, 40)	(2, 6)	(1, 10)	96.0 ± 3.7	96.8 ± 1.6
14	(10, 40)	(2, 6)	(1, 20)	98.8 ± 1.6	99.0 ± 1.1

Parentheses represent ranges: (lower bound, upper bound). 'Test' is the accuracy (mean ± s.d.) on the test problem (here, the London Underground map) after the completion of each intermediate lesson. Evaluation of lesson completion occurs after every group of 100 batches has been processed on the main worker thread. The completion threshold is met if 90% of modal samples (most likely output of the network) are correct. 'Final' (mean ± s.d.) is the accuracy on the final lesson of the curriculum after the completion of each intermediate lesson.

**Extended Data Table 4 | Curriculum results for inference**

Lesson	Nodes	Out-degree	Relation Length	Queries	Test	Final
1	(3, 3)	(2, 2)	(2, 2)	3	0.0± 0.0	2.3± 2.3
2	(3, 5)	(2, 2)	(2, 2)	3	0.0± 0.0	8.9± 4.9
3	(4, 6)	(2, 4)	(2, 2)	3	0.0± 0.0	14.3± 5.7
4	(4, 6)	(2, 4)	(2, 2)	3	0.0± 0.0	15.5± 6.2
5	(6, 12)	(2, 4)	(2, 2)	3	0.0± 0.0	22.7± 3.6
6	(12, 18)	(2, 4)	(2, 2)	3	0.0± 0.0	25.8± 1.6
7	(4, 6)	(2, 4)	(2, 3)	3	0.1± 0.2	31.2± 4.0
8	(6, 12)	(2, 4)	(2, 3)	3	0.1± 0.2	47.6± 5.5
9	(12, 18)	(2, 4)	(2, 3)	3	0.0± 0.0	55.1± 2.5
10	(4, 6)	(2, 4)	(2, 4)	3	14.5± 14.2	49.4± 5.0
11	(4, 6)	(2, 4)	(2, 4)	3	11.1± 13.1	50.2± 5.8
12	(6, 12)	(2, 4)	(2, 4)	3	20.4± 13.5	66.3± 4.0
13	(12, 18)	(2, 4)	(2, 4)	3	43.6± 19.5	78.9± 2.9
14	(4, 6)	(2, 4)	(2, 5)	3	18.6± 13.8	68.6± 5.0
15	(6, 12)	(2, 4)	(2, 5)	3	26.4± 19.1	78.4± 3.8
16	(12, 18)	(2, 4)	(2, 5)	3	61.9± 20.5	91.1± 2.3
17	(20, 25)	(2, 4)	(2, 5)	3	81.8± 13.5	95.5± 2.2

Parentheses represent ranges: (lower bound, upper bound). Evaluation of lesson completion occurs after every group of 100 batches has been processed on the main worker thread. The completion threshold is met if 90% of modal samples (most likely output of the network) are correct. 'Test' and 'Final' as in Extended Data Table 3.

**Extended Data Table 5 | Curriculum results for shortest-path task**

Lesson	Nodes	Out-degree	Path Length	Test	Final
1	(5, 10)	(1, 2)	(2, 2)	8.2 ± 6.1	11.2 ± 4.5
2	(5, 20)	(1, 2)	(2, 2)	4.0 ± 2.6	12.7 ± 4.8
3	(10, 20)	(1, 2)	(2, 2)	4.0 ± 3.7	13.2 ± 5.1
4	(10, 20)	(1, 2)	(2, 3)	9.5 ± 5.7	13.7 ± 3.6
5	(10, 20)	(1, 3)	(2, 3)	14.4 ± 5.7	22.8 ± 7.1
6	(10, 20)	(2, 3)	(2, 3)	14.2 ± 6.8	27.6 ± 9.2
7	(10, 20)	(2, 3)	(2, 4)	28.3 ± 9.0	34.0 ± 6.7
8	(10, 20)	(2, 4)	(2, 4)	29.2 ± 9.0	39.7 ± 7.4
9	(10, 25)	(2, 4)	(2, 4)	34.4 ± 11.4	41.1 ± 9.4
10	(10, 25)	(2, 4)	(2, 5)	32.3 ± 10.3	40.0 ± 8.4
11	(10, 25)	(2, 4)	(2, 5)	33.8 ± 9.7	44.0 ± 11.4
12	(15, 25)	(2, 4)	(2, 5)	34.9 ± 7.0	47.2 ± 8.0
13	(15, 25)	(2, 5)	(2, 5)	40.9 ± 6.6	54.3 ± 5.6
14	(20, 25)	(2, 5)	(2, 5)	50.7 ± 2.7	54.0 ± 9.6
15	(20, 25)	(2, 6)	(2, 5)	55.3 ± 6.4	64.0 ± 3.5

Parentheses represent ranges: (lower bound, upper bound). Evaluation of lesson completion occurs after every group of 2,000 batches (size 1) has been processed on main worker thread. A path is defined as 'correct' if it is a shortest path. The completion threshold is met if 80% of modal samples (most likely output of the network) are correct on a new group of 50 episodes. 'Test' and 'Final' as in Extended Data Table 3.



**Extended Data Table 6 | Curriculum for Mini-SHRDLU**

Lesson	Number of goals	Number of blocks	Number of constraints	Search depth
1	(2, 2)	(3, 3)	(1, 1)	(1, 1)
2	(2, 3)	(3, 4)	(1, 2)	(1, 1)
3	(3, 3)	(4, 4)	(1, 2)	(1, 2)
4	(3, 4)	(4, 5)	(2, 2)	(1, 2)
5	(4, 4)	(5, 5)	(2, 2)	(2, 2)
6	(4, 5)	(5, 6)	(2, 3)	(2, 2)
7	(5, 5)	(6, 6)	(2, 3)	(2, 3)
8	(5, 6)	(6, 6)	(3, 3)	(2, 3)
9	(6, 6)	(6, 6)	(3, 3)	(3, 3)
10	(6, 7)	(6, 6)	(3, 4)	(3, 3)
11	(7, 7)	(6, 6)	(3, 4)	(3, 4)
12	(7, 8)	(6, 6)	(4, 4)	(3, 4)
13	(8, 8)	(6, 6)	(4, 4)	(4, 4)
14	(8, 9)	(6, 6)	(4, 5)	(4, 4)
15	(9, 9)	(6, 6)	(4, 5)	(4, 5)
16	(9, 10)	(6, 6)	(5, 5)	(4, 5)
17	(10, 10)	(6, 6)	(5, 5)	(5, 5)
18	(10, 10)	(6, 6)	(5, 6)	(5, 5)
19	(10, 10)	(6, 6)	(5, 6)	(5, 6)
20	(10, 10)	(6, 6)	(6, 6)	(5, 6)
21	(10, 10)	(6, 6)	(6, 6)	(6, 6)
22	(10, 10)	(6, 6)	(6, 6)	(6, 6)
23	(10, 10)	(6, 6)	(6, 6)	(6, 7)
24	(10, 10)	(6, 6)	(6, 6)	(6, 7)
25	(10, 10)	(6, 6)	(6, 6)	(7, 7)
26	(10, 10)	(6, 6)	(1, 6)	(1, 7)

Parentheses represent ranges: (lower bound, upper bound). Evaluation of lesson completion occurs after every group of 400 batches has been processed on the main worker thread. The completion threshold is met if 85% of constraints are satisfied at episode termination on average over 160 episodes. (The final lesson has no termination.)